



### Request for Quote and Specifications of bare silicon wafers

- The GEECI (Gallium Nitride Ecosystem Enabling Centre and Incubator) at SID-Indian Institute of Science is seeking bids from qualified vendors for bare silicon wafers with the specifications mentioned below in the table below.
- Companies need to submit two bids, a technical bid and a commercial bid, in **two separate** sealed envelopes. The bids should be submitted no later than 30 days from the date of posting of this tender, as listed on the website date/time stamp, and by 5 pm on the 30<sup>th</sup> day or next weekday in case the 30<sup>th</sup> day falls on a weekend or a national holiday.
- Both technical and commercial bids should be addressed to “The Chief Executive, SID, IISc, Bangalore 560012, GST # 29AAATS5333E1ZJ.”
- All quotations should be CIF Bangalore.
- Cost of last mile transportation, including any insurance, from port of shipment to IISc has to be quoted as an option.
- In case of courier shipments maximum permissible weight would be 70kgs.
- The envelopes should be addressed to “Prof. Srinivasan Raghavan, CeNSE, IISc, Bangalore, 560012” and submitted to the office at CeNSE, IISc in Room No. GF 15 between 9 am and 5 pm.
- All questions regarding this tender should be addressed to Prof. Srinivasan Raghavan at the email address [sraghavan@iisc.ac.in](mailto:sraghavan@iisc.ac.in)
- Post such submission all vendors should send an email to [sraghavan@iisc.ac.in](mailto:sraghavan@iisc.ac.in) with the subject line: “GEECI\_Bidder’s name\_Tool Name” to intimate him of the submission within one day.
- Deviations from the technical specifications requested are allowed. Such deviations must be highlighted and justified. Their acceptance or rejection will be left to the discretion of the technical committee.
- The consumable sought will be used at the Centre for Nano Science and Engineering (CeNSE), Indian Institute of Science (IISc). IISc is India’s No. 1 institution on higher learning and the Center for Nano Science and Engineering is home to one of the best academic fabs in the world.
- The technical response, corresponding to the consumable being offered, should be in the form of a compliance table with at least 5 columns. Serial number in column 1. Each of the numbered technical items below should be addressed in a separate row of the table in column 2. Compliance to this requirement, in Yes/No, deviation from it and justification should be provided in the neighboring columns 3-5. Post the opening of a hard copy of the technical bid the committee will request for a soft copy of the files for further processing. Companies should **NOT** mail soft copies of the files unless specifically requested for.
- Detailed technical specifications of the consumable being offered should be included.
- Any additional capabilities or technical details, that you would like to bring to the attention of the purchase committee, can be listed at the end of the technical table.
- If multiple systems can fulfill the requirements, vendors can submit multiple bids.
- Vendors are encouraged to highlight the advantages of their consumables over comparable consumables from the competitors.
- The commercial bid should be broken up to the maximum extent possible into separate items with a cost against each to enable better comparison of price for various configurations across the bidders. As an option, please provide itemized cost for any *suggested* accessories/add-ons that may enhance the usability, capability, accuracy or reliability of the tool. Vendors are encouraged to quote for as many add-ons as their consumable portfolio permits.

Primary description	Silicon wafers, FZ, Prime Grade
Number of wafers required	200 (two hundred)
Size	2 inch (~ 50 mm) in diameter
Orientation	<111>
Resistivity	(Undoped), > 10,000 Ohm-cm
Primary flat length	15.88 mm
Primary flat orientation	<110>
Bow	< 20 $\mu$ m
Thickness	1000 $\mu$ m
Total thickness variation	< 10 $\mu$ m
Surface roughness	< 1 nm
Front surface finish	polished
Back surface finish	etched
<b>Common Terms and Conditions</b>	
<b>Shipping:</b> The cost of shipping up to IISc should be included. IISc will help with customs clearance at Bangalore Airport. Please include your payment option. IISc would prefer to retain at least 20% of payment till wafers have been received at IISc	